

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1228883
<b>Application Number:</b>	10803792
<b>Confirmation Number:</b>	5596
<b>Title of Invention:</b>	Semiconductor integrated circuit having a number of data output pins capable of selectively providing output signals and test method thereof
<b>First Named Inventor:</b>	Nam-Jung Her
<b>Customer Number:</b>	20575
<b>Filer:</b>	Hosoon Lee/Stormi Davis
<b>Filer Authorized By:</b>	Hosoon Lee
<b>Attorney Docket Number:</b>	4591-363
<b>Receipt Date:</b>	29-SEP-2006
<b>Filing Date:</b>	17-MAR-2004
<b>Time Stamp:</b>	18:18:38
<b>Application Type:</b>	Utility
<b>International Application Number:</b>	

### Payment information:

Submitted with Payment	no
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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		4591363a.pdf	555684	yes	9

	Multipart Description		
	Doc Desc	Start	End
	Miscellaneous Incoming Letter	1	1
	Amendment - After Non-Final Rejection	2	9

**Warnings:**

**Information:**

**Total Files Size (in bytes):**

555684

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**New Applications Under 35 U.S.C. 111**

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

**National Stage of an International Application under 35 U.S.C. 371**

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.